

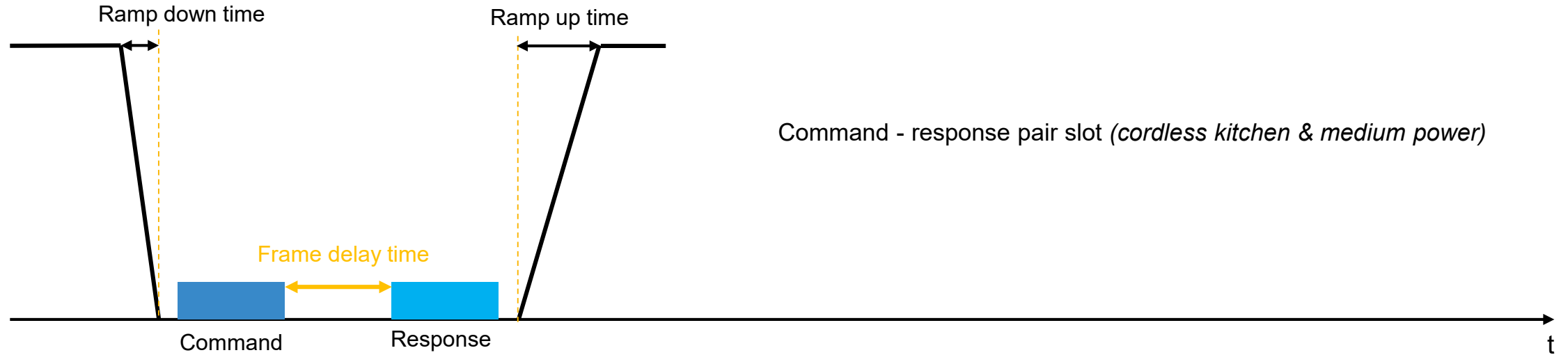
MINIMUM SLOT DURATION ANALYSIS FOR NFC COMMUNICATION

July 15th

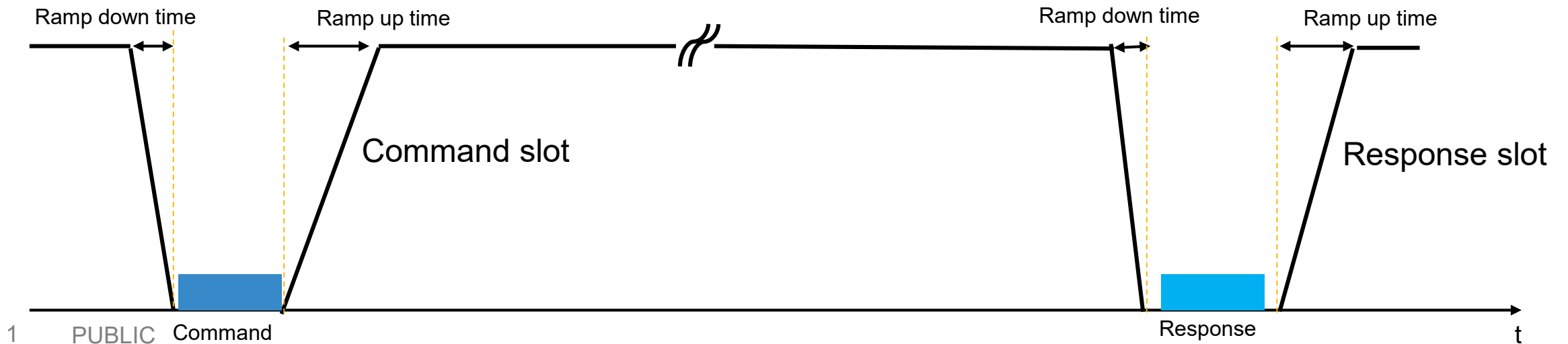


Definitions

1. CMD-RES within a single slot

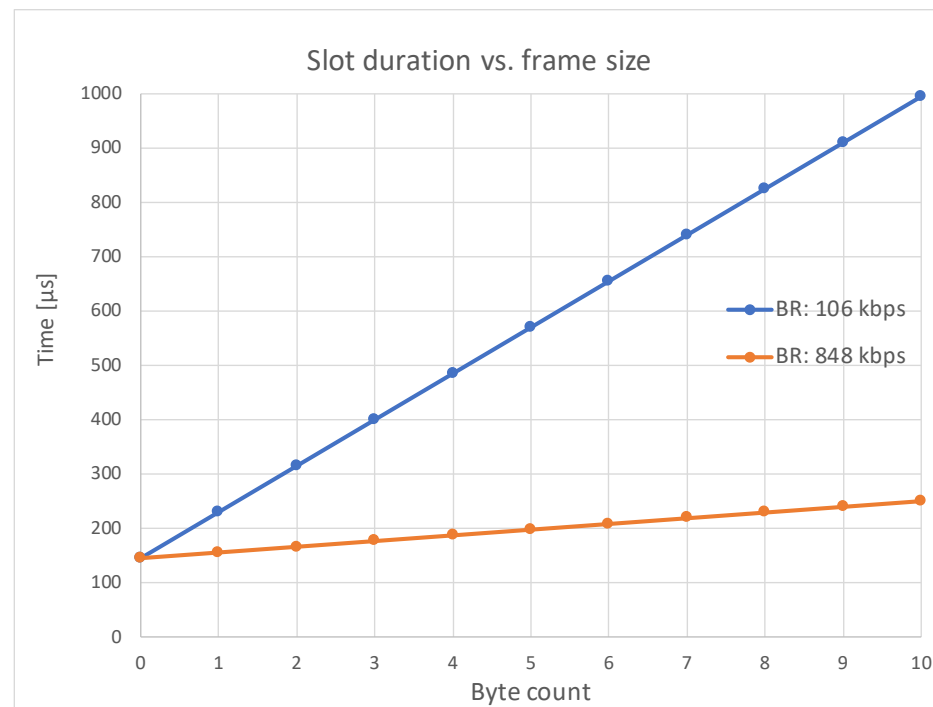


2. Dedicated slots for CMD and RES



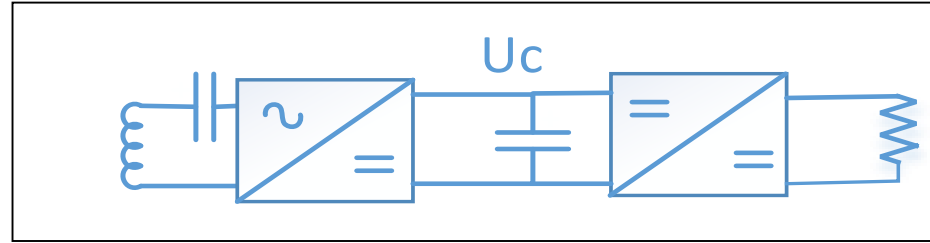
Dedicated response slot duration analysis

- For WLC the response contains the longest frames (largest amount of data) – worst case
- Slot time calculation is valid for a slot containing the response only
- Constant timing parameters:
 - Ramp down time: $25\mu\text{s}$
 - Ramp up time: $100\mu\text{s}$
 - $10\mu\text{s}$ NFC carrier added before response for NFC carrier detection by PRx
 - $10\mu\text{s}$ NFC carrier added after response for end of frame detection by PTx



Required capacitance analysis to buffer slot duration

Block diagram:



Formula's:

$$E_{cap} = \frac{P_{load} * t_{slot}}{\eta}$$

$$\frac{C}{2} (U_C^2(t_1) - U_C^2(t_{max})) = E_{cap}$$

$$C = \frac{2 * E_{cap}}{(U_C^2(t_1) - U_C^2(t_{max}))}$$

Parameter:

Parameter	value
Nominal capacitor voltage $U_C t_1$ [V]	30
Minimum capacitor voltage $U_C t_{max}$ [V]	20
Power delivered to load P_{load} [W]	65
Worst case DC-DC efficiency η [-]	0,9

